

A1 09/493,978, filed January 28, 2000, entitled "SYSTEM METHOD FOR CONTROLLED POLISHING AND PLANARIZATION OF SEMICONDUCTOR WAFERS," by Rod Kistler and Yehiel Gotkis, and such related application is hereby incorporated by reference.--

REMARKS

The Applicants are hereby amending the specification to correct the application number which was provided erroneously as 09/664,135, but should have been 09/644,135.

Respectfully Submitted,
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